

FIG. 3

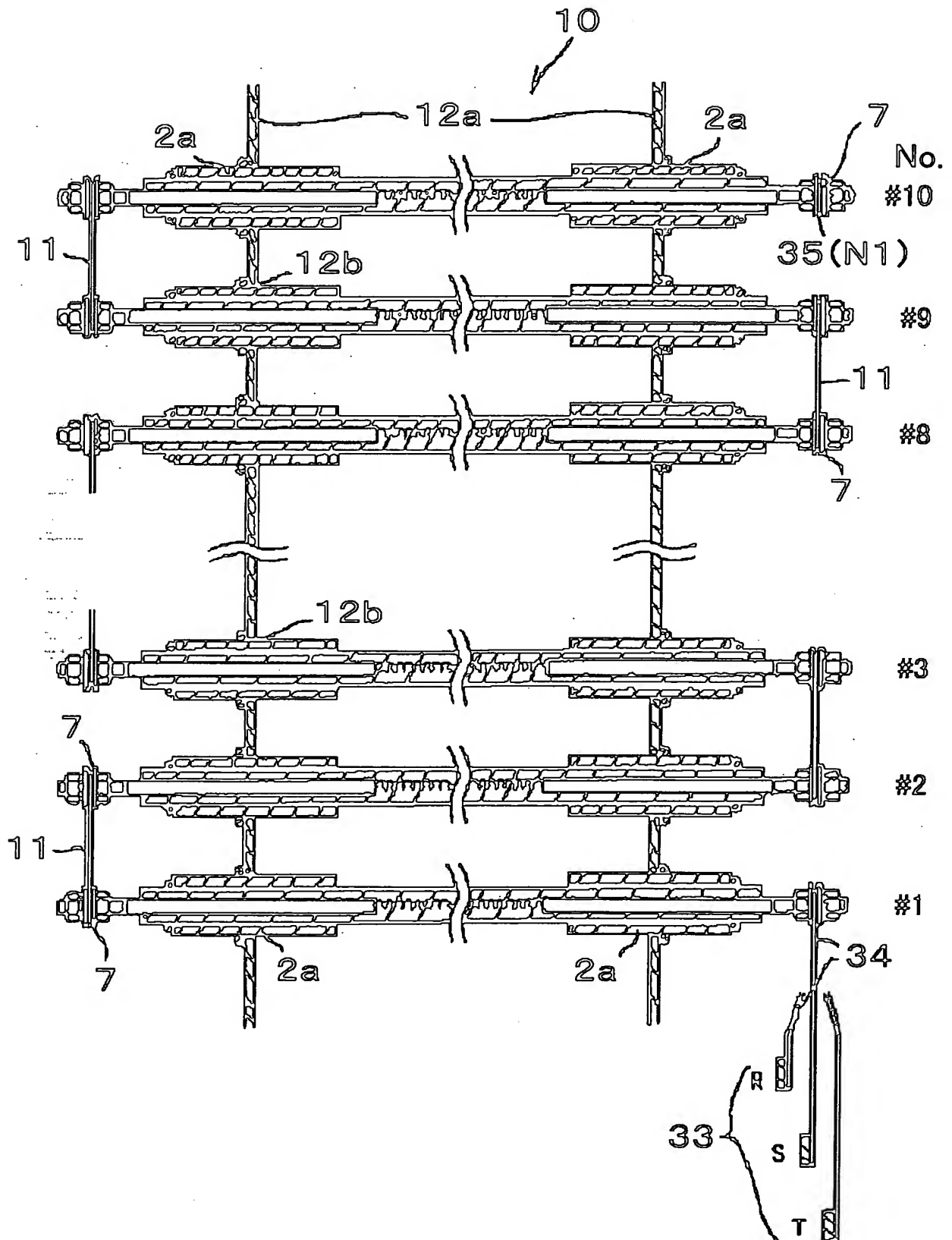


FIG. 4

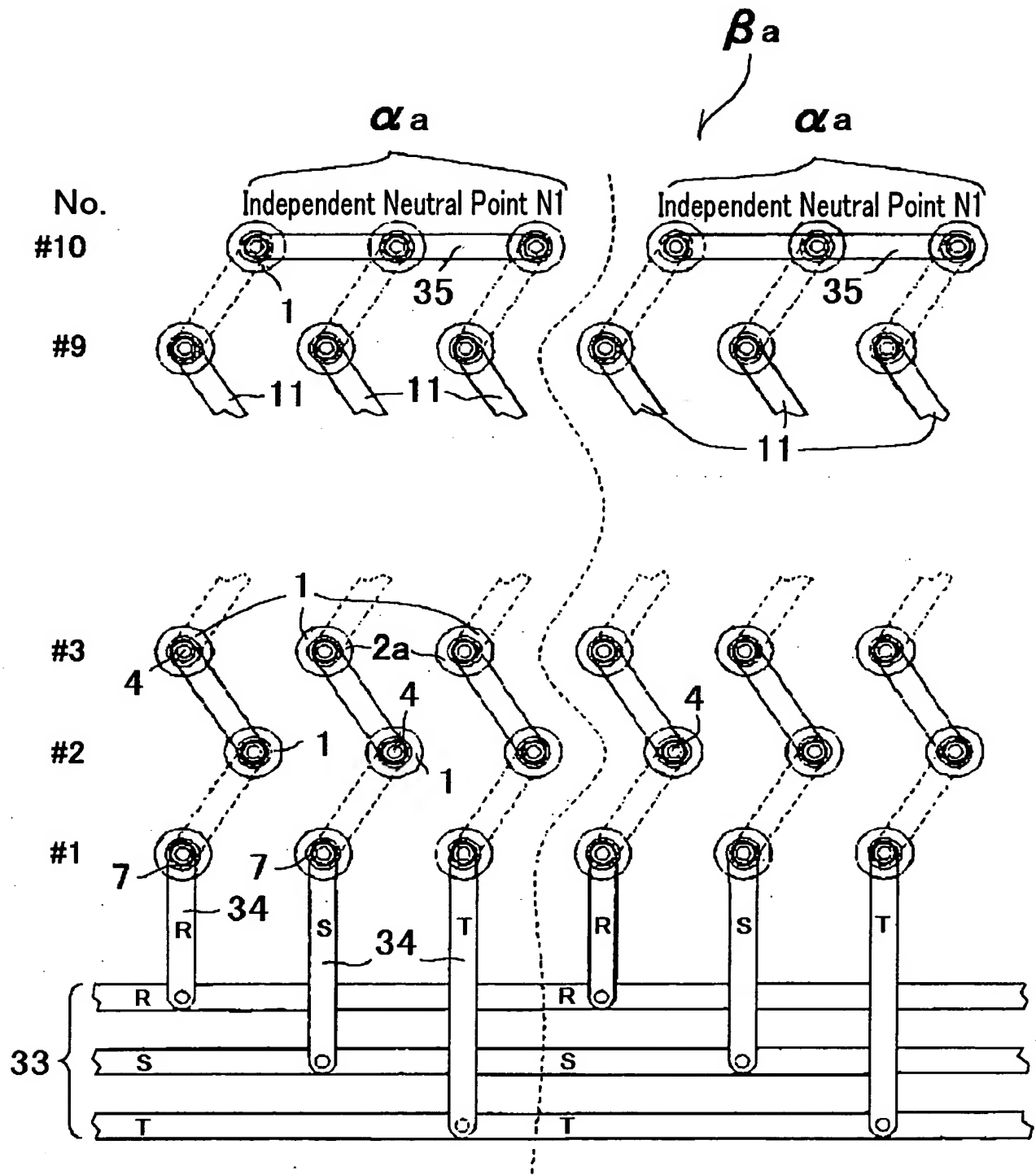


FIG. 5

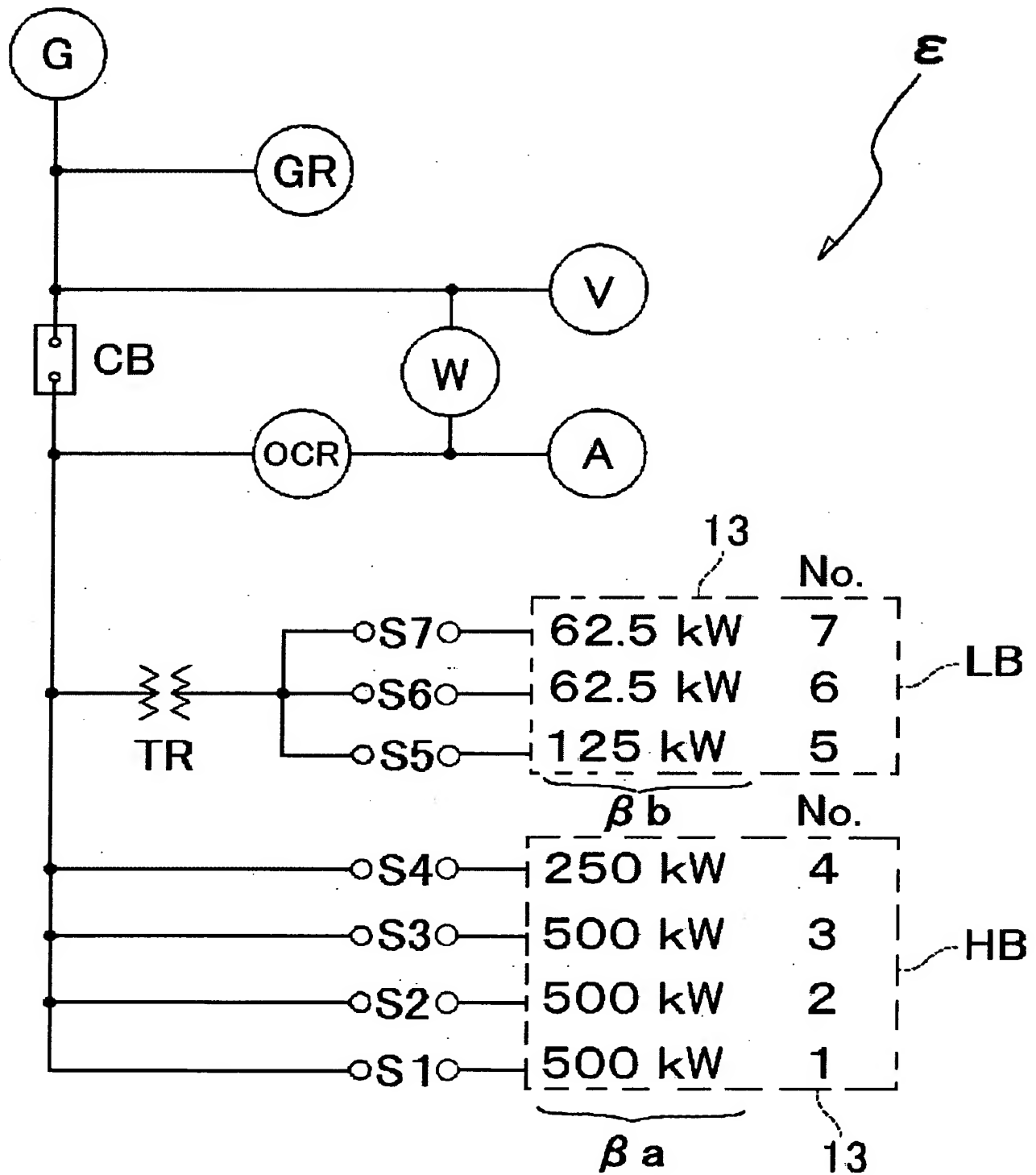


FIG. 6

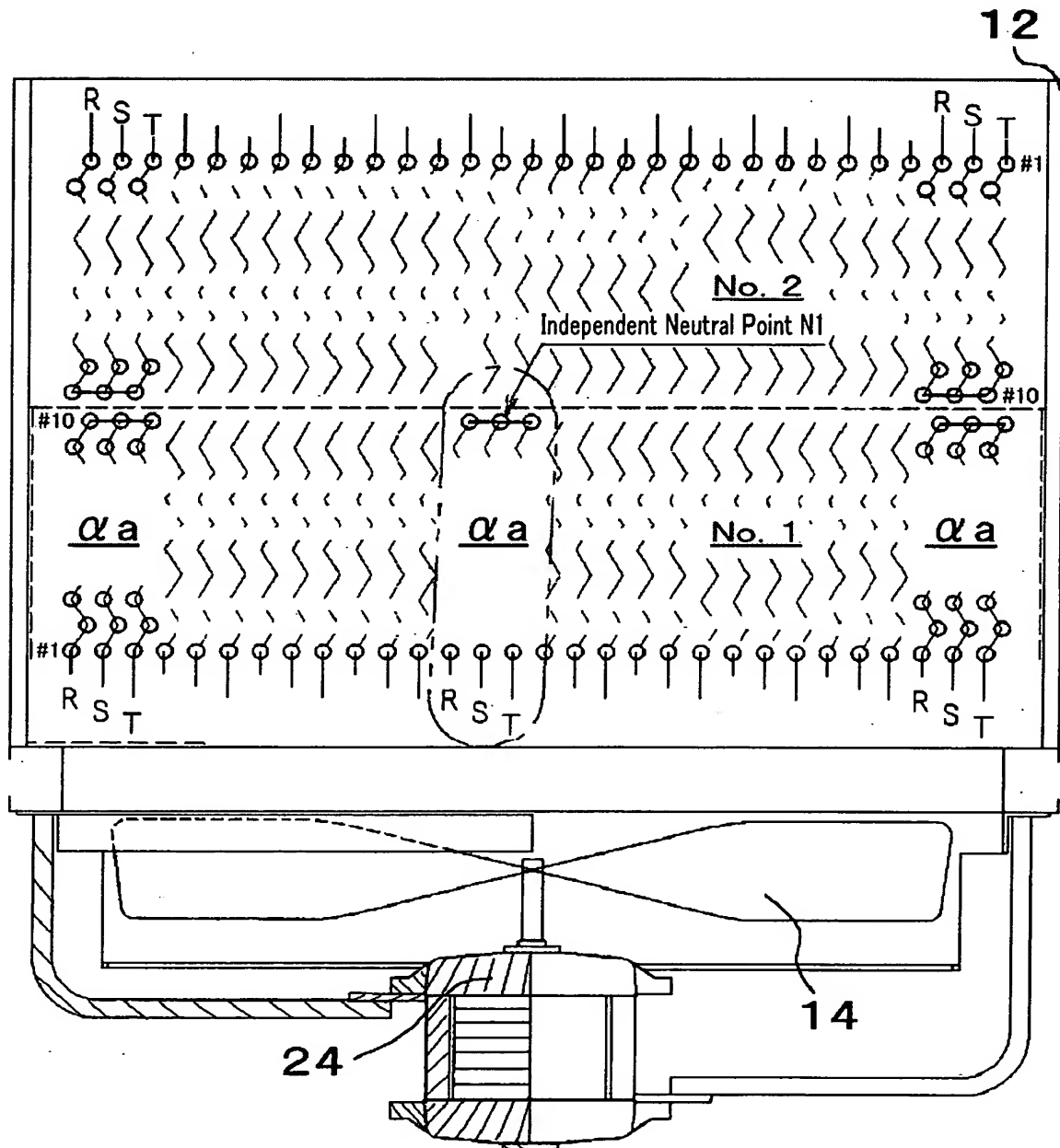


FIG. 7

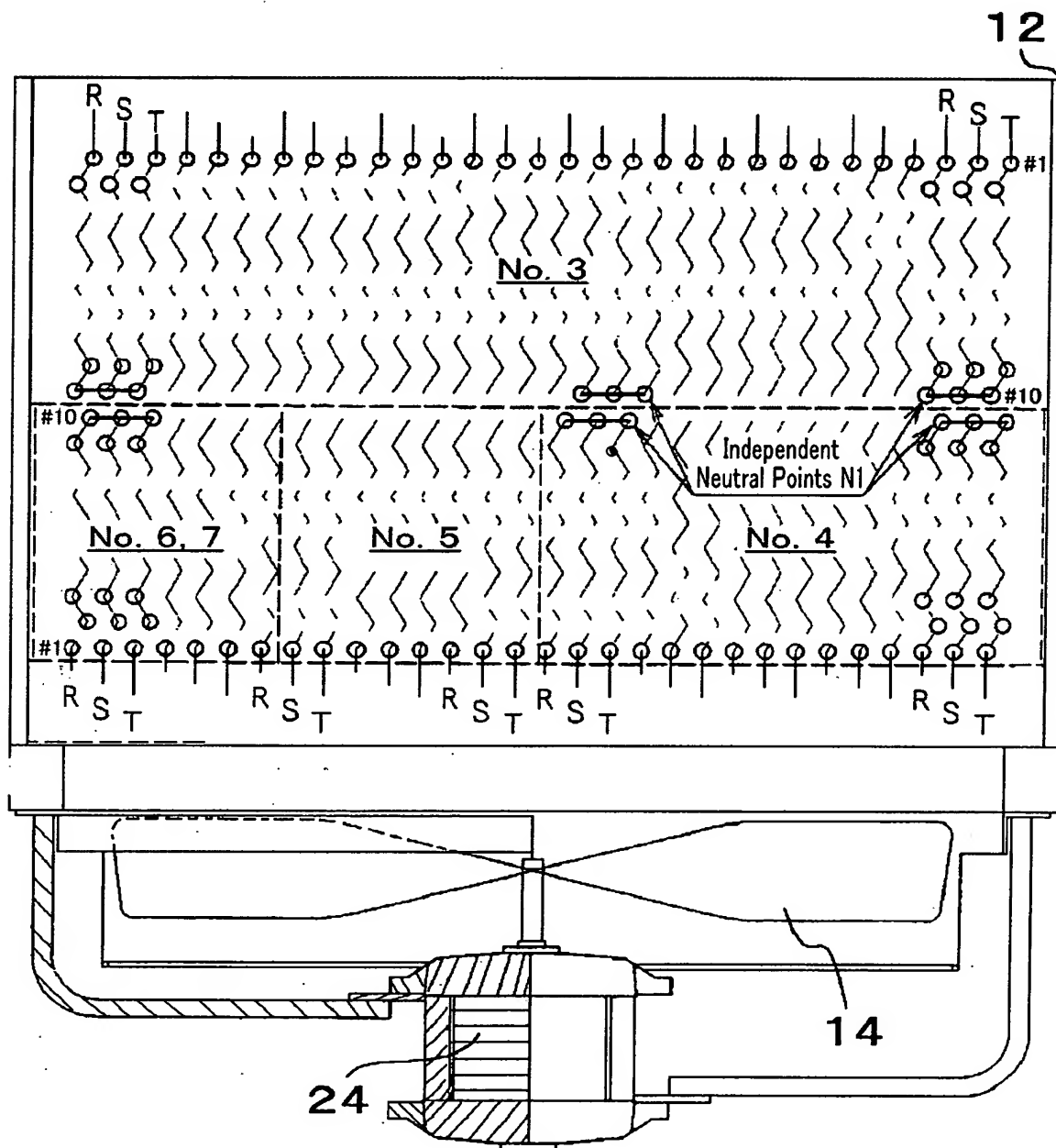


FIG. 8

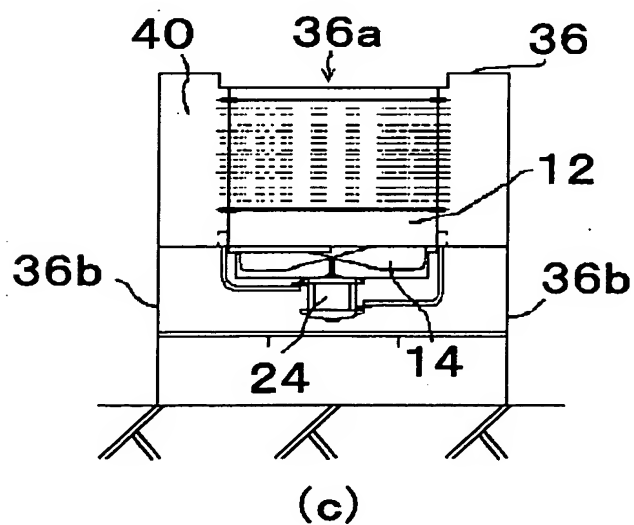
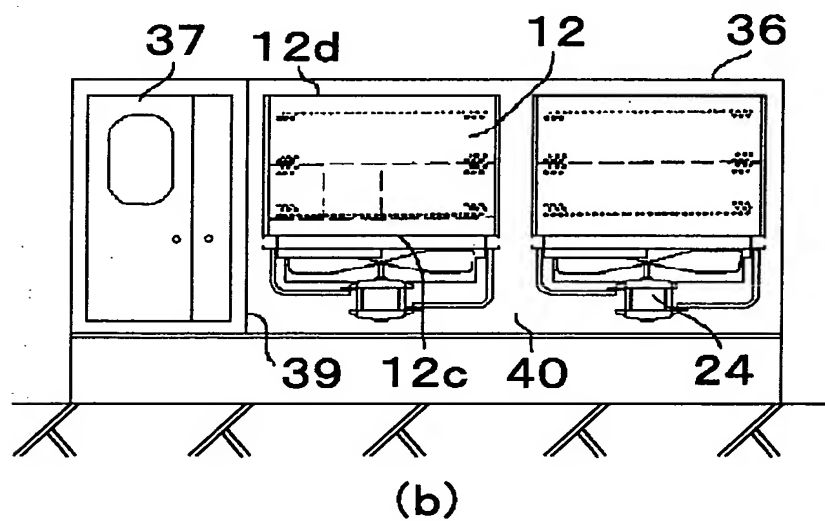
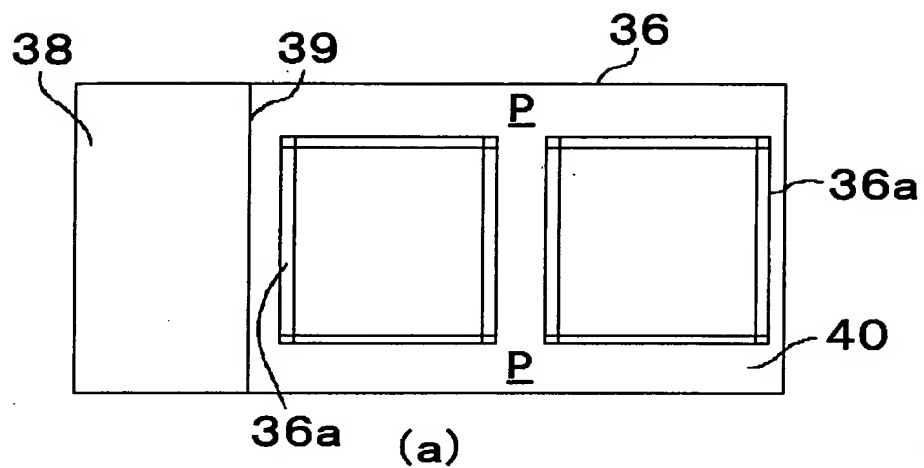
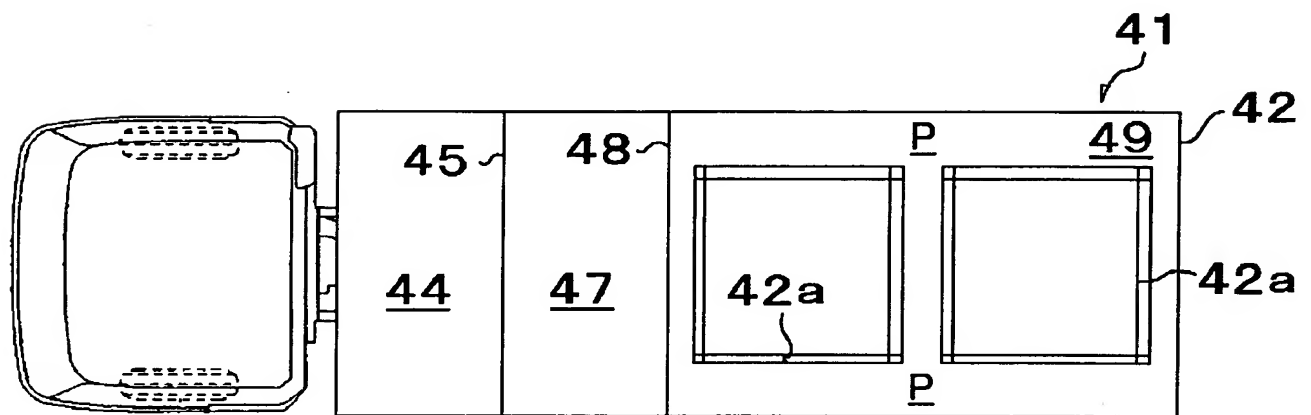
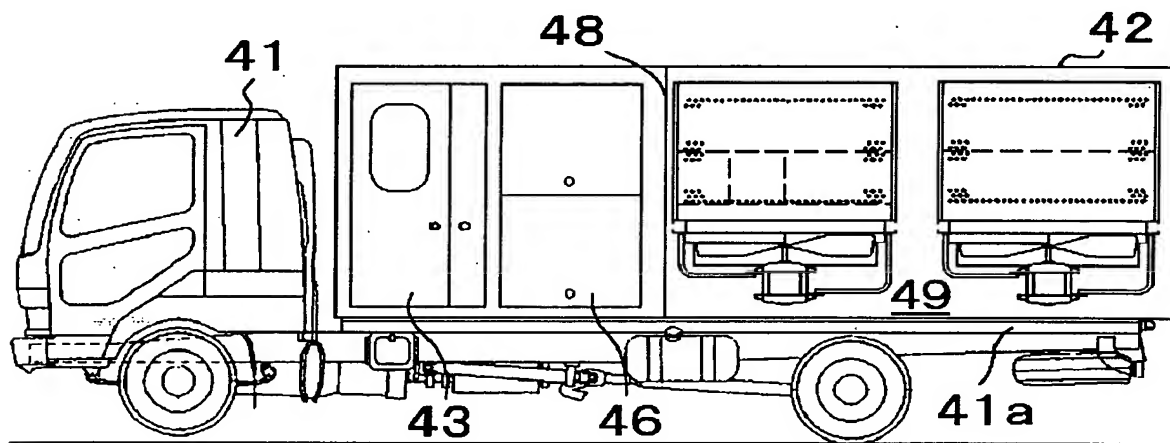


FIG. 9

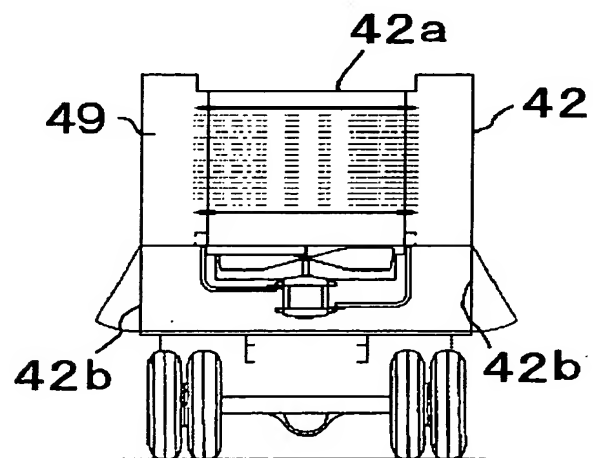
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(a)



(b)



(c)

FIG. 10

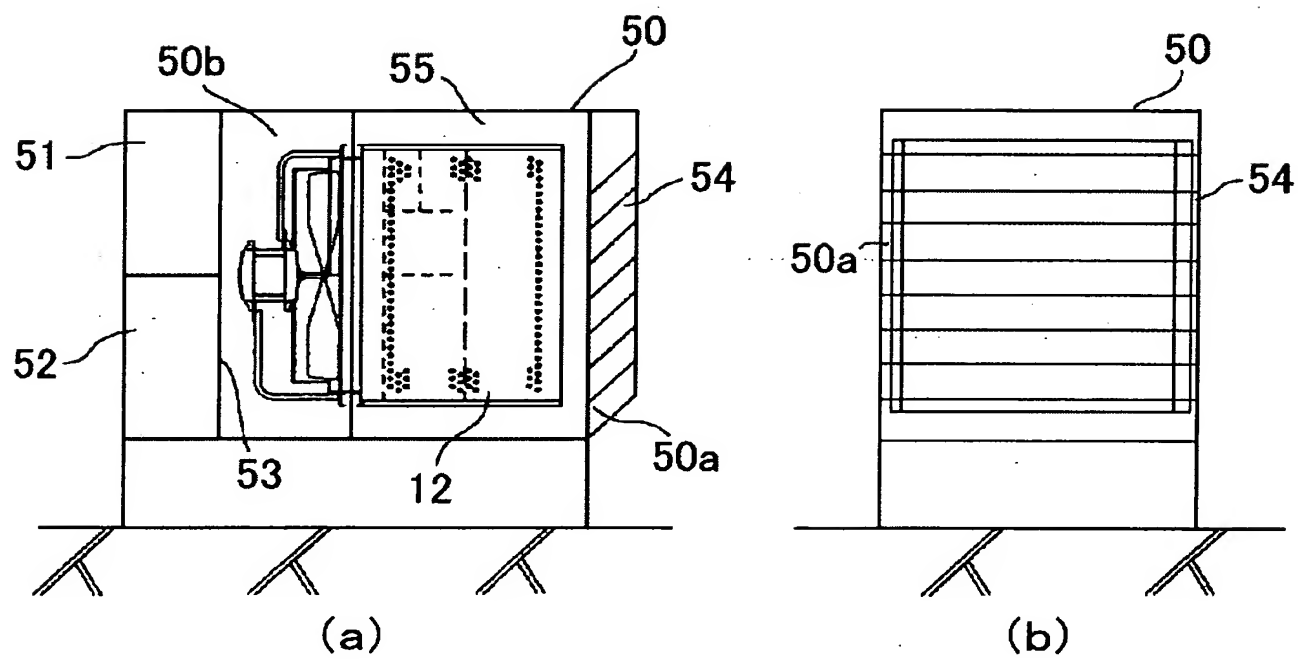


FIG. 11

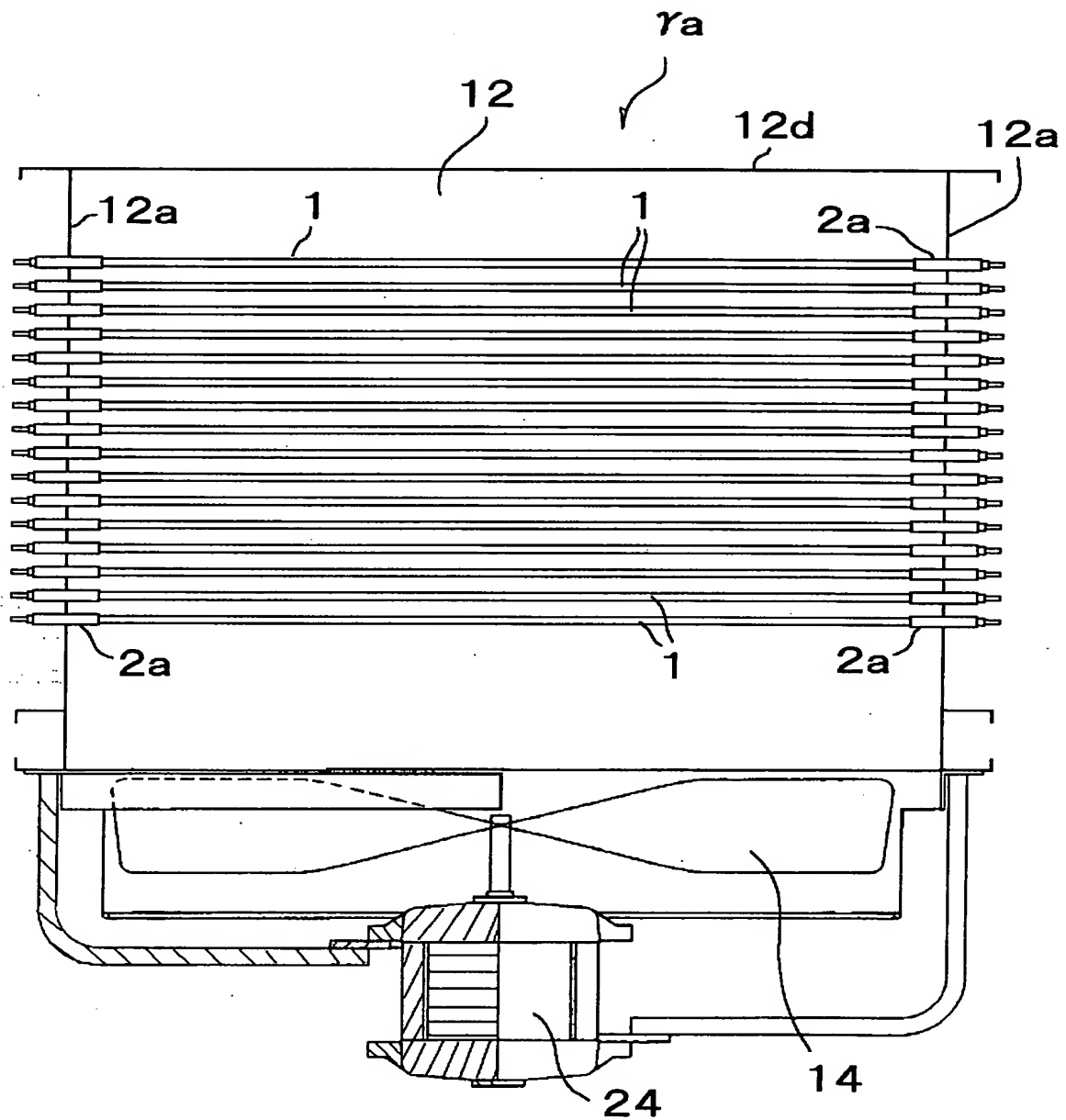


FIG. 12

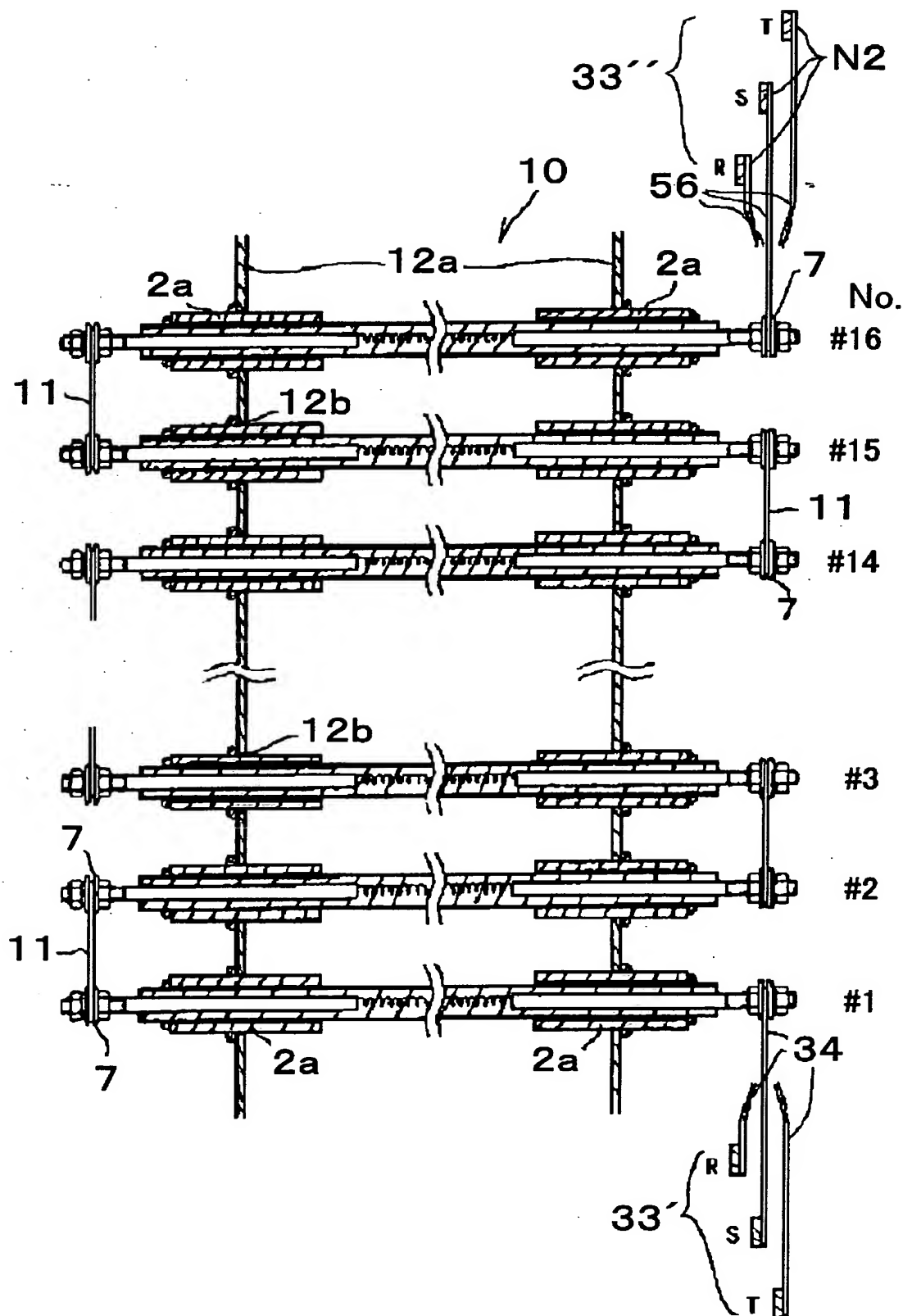


FIG. 13

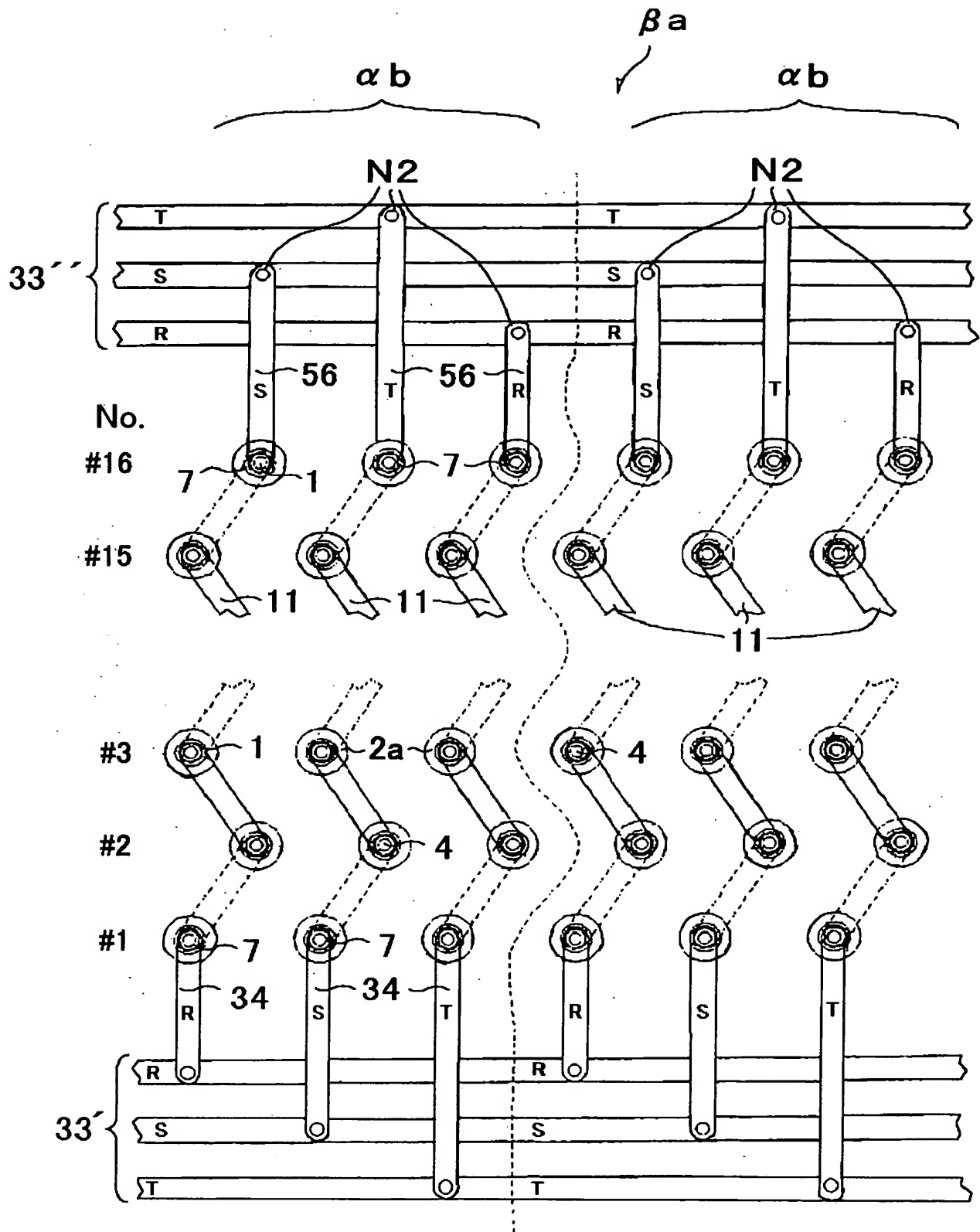


FIG. 14

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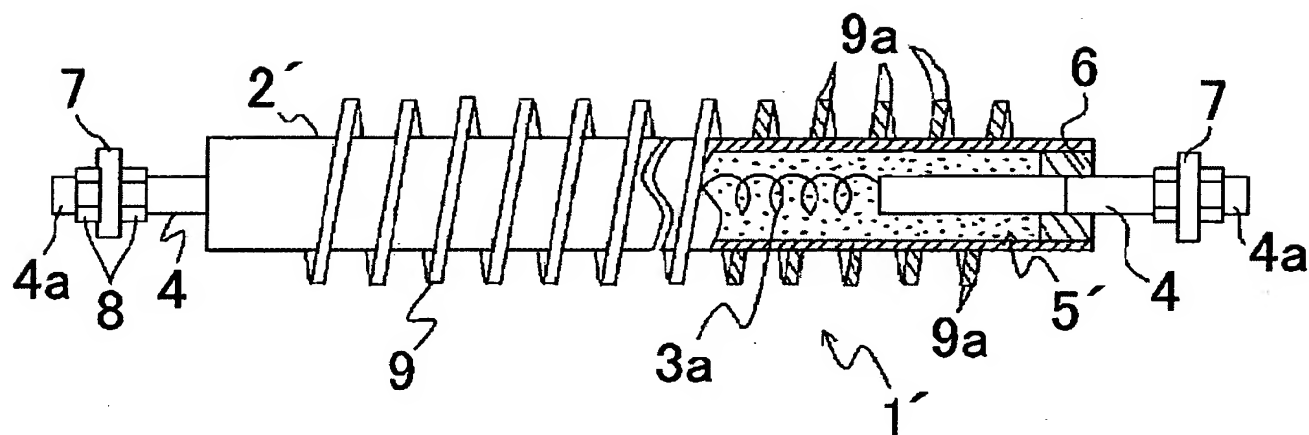


FIG. 15

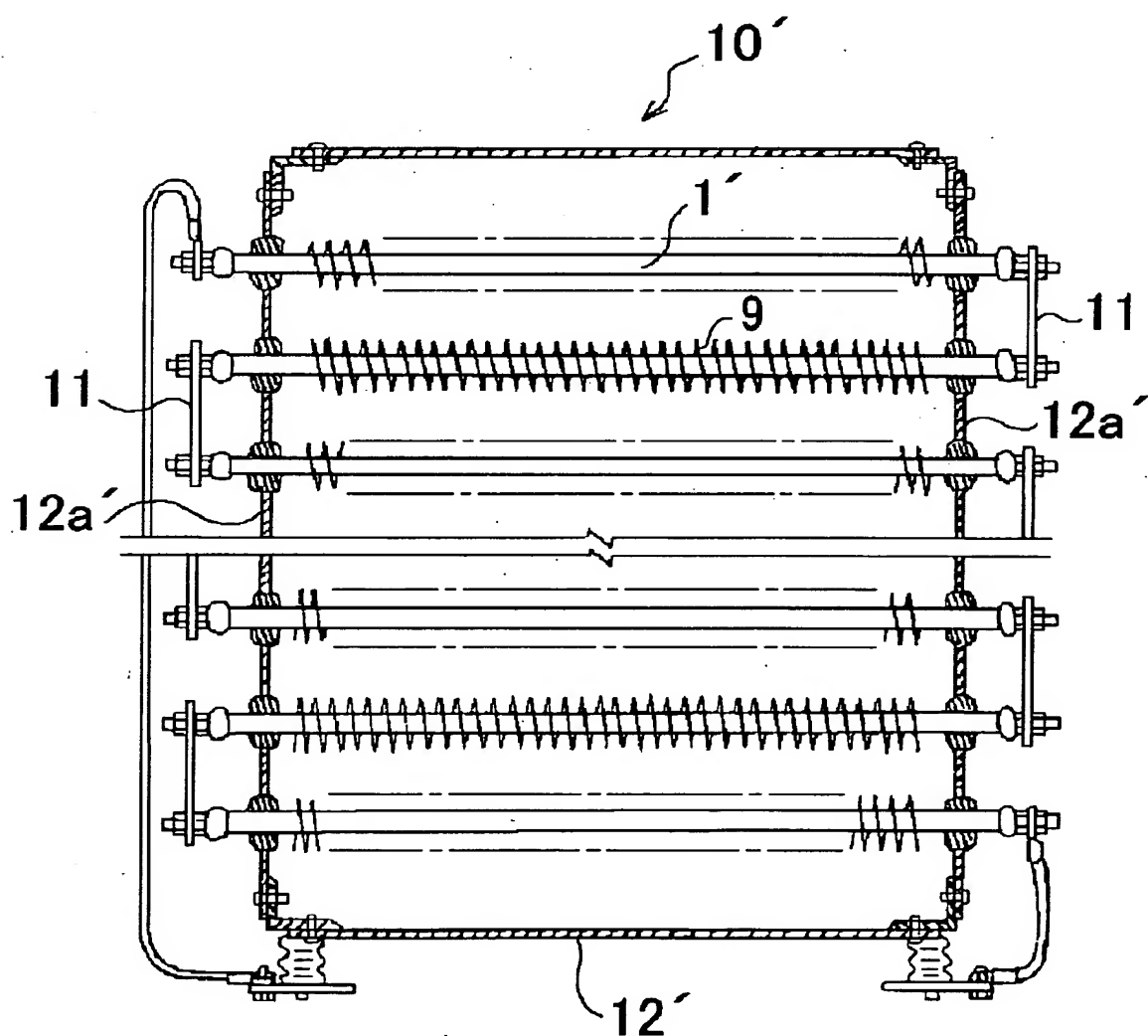


FIG. 16

FIG. 17

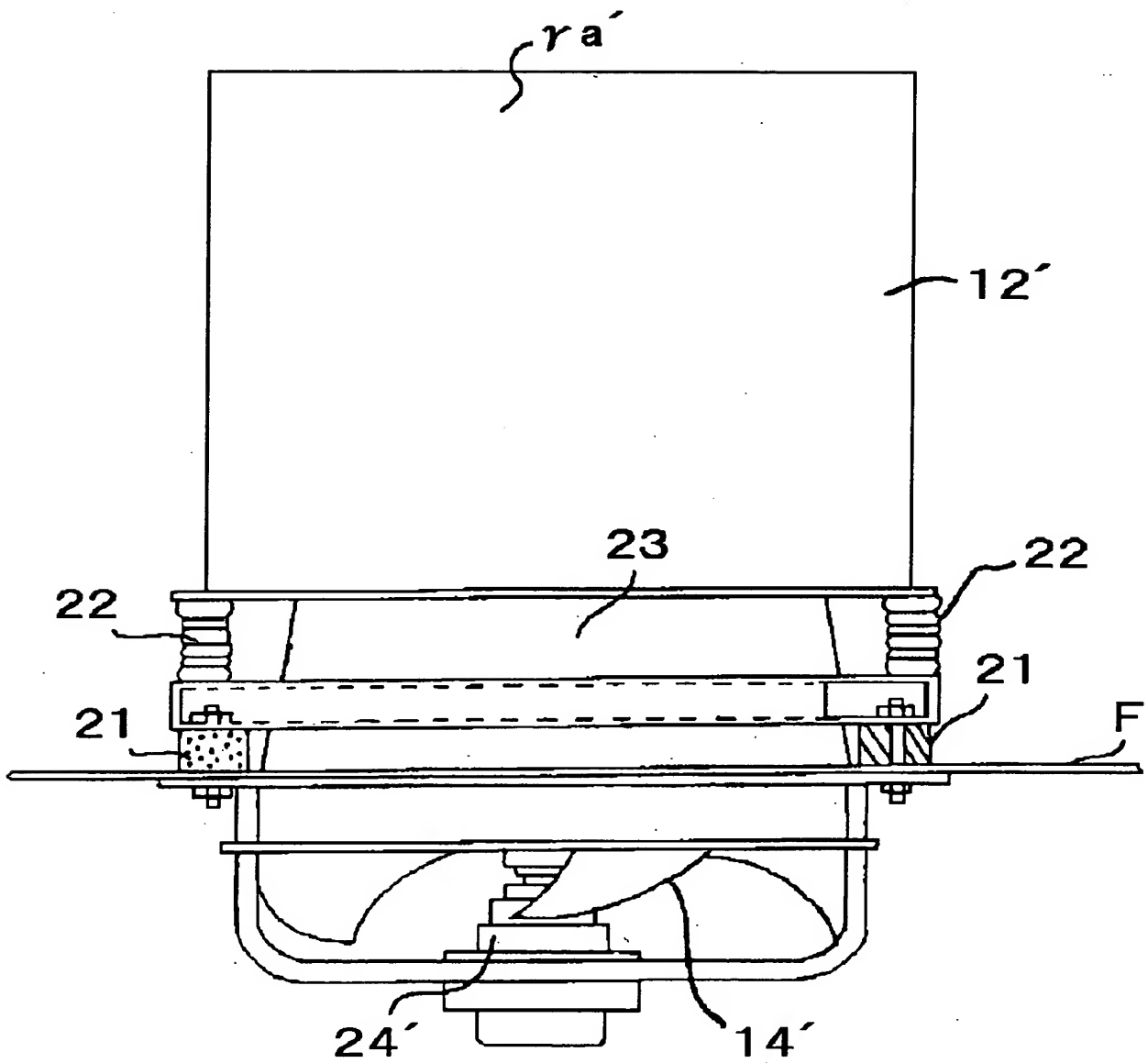


FIG. 18

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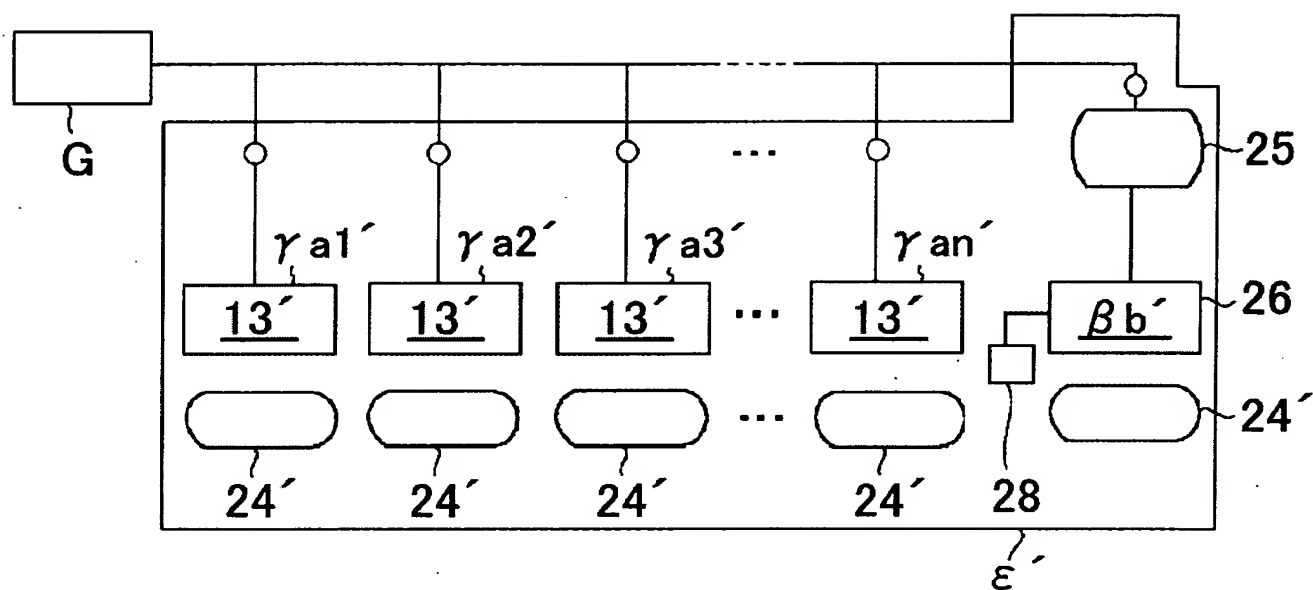


FIG. 19

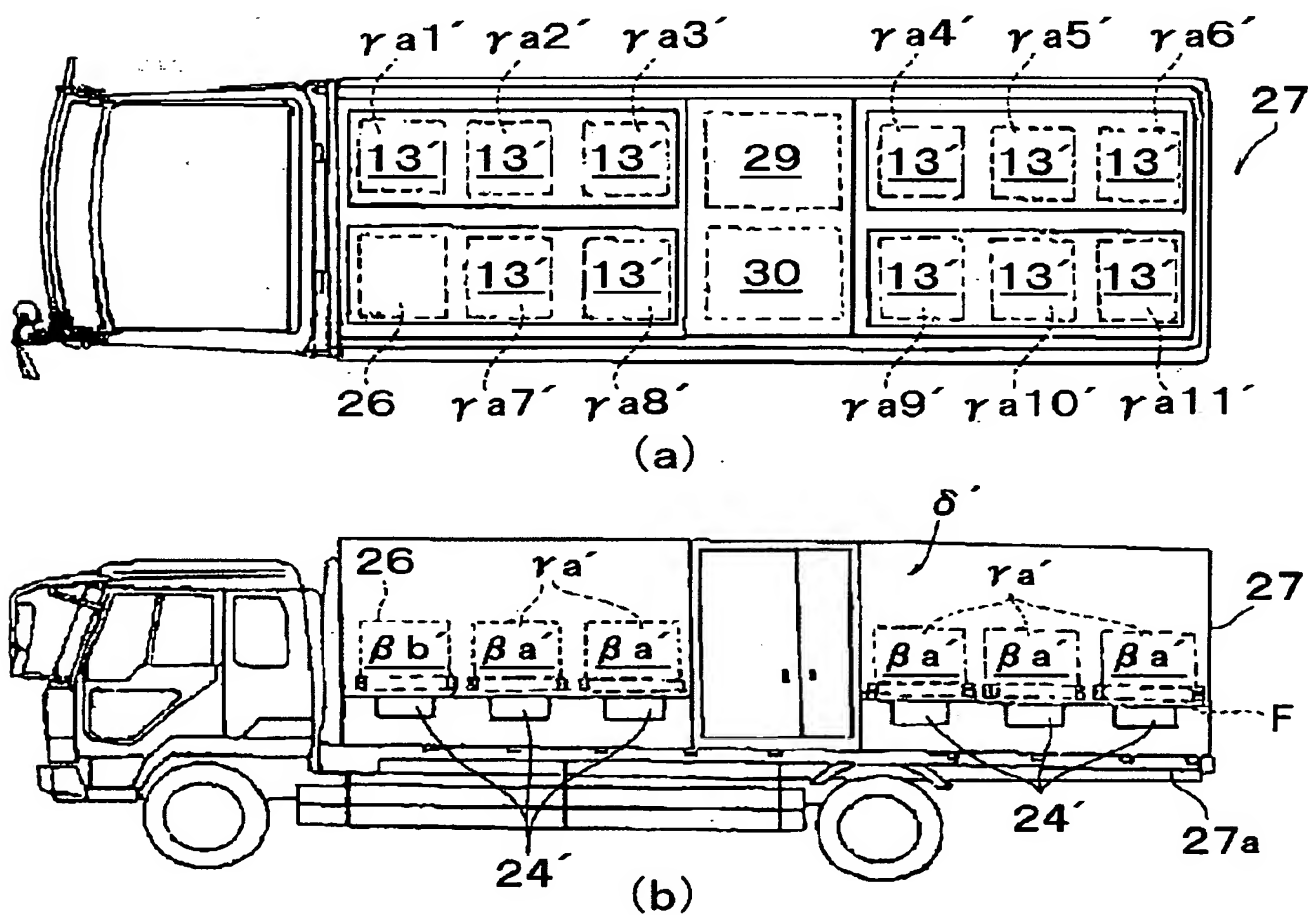


FIG. 20

Equivalent Circuit

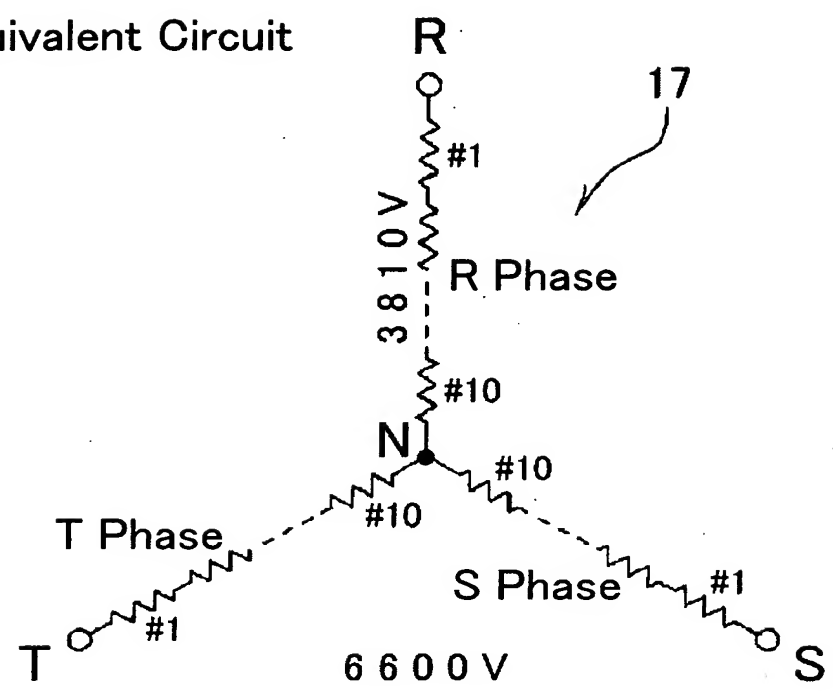


FIG. 21

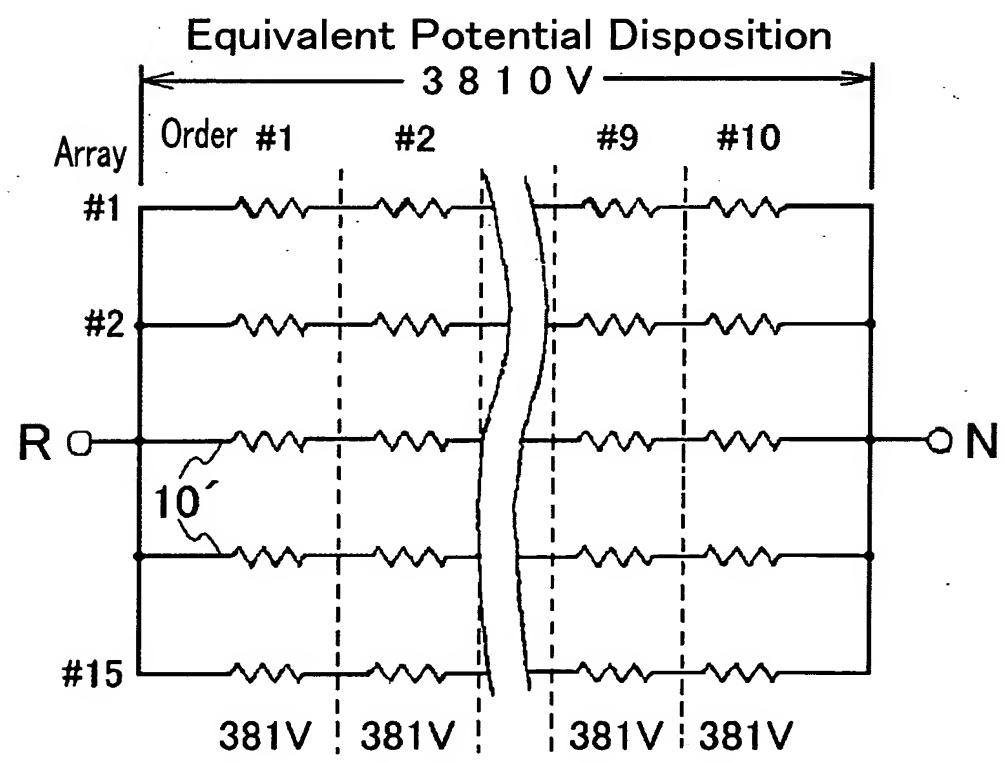


FIG. 22

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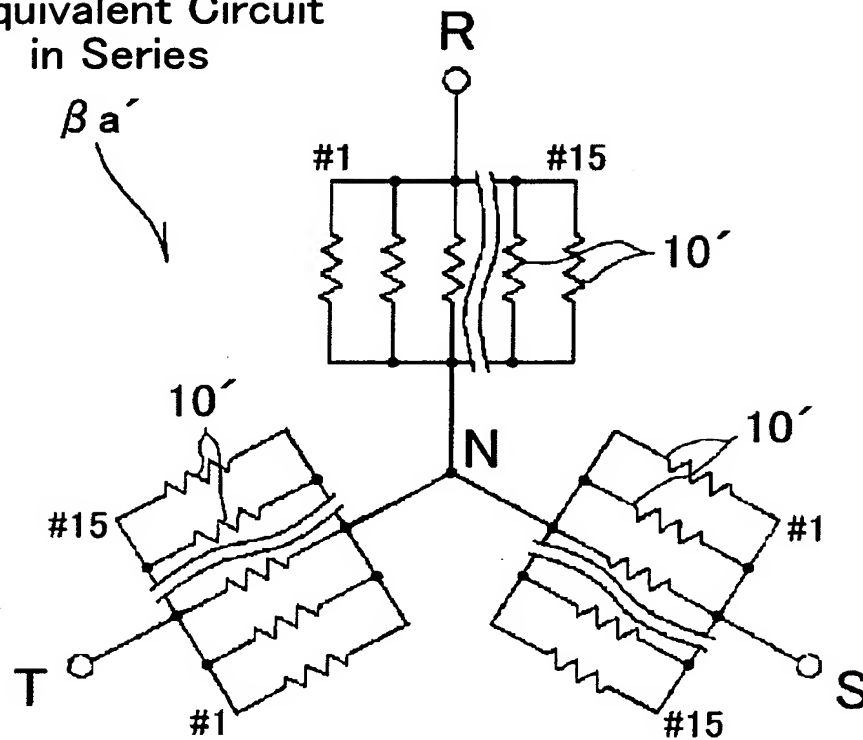
Y Equivalent Circuit
in Series

FIG. 23

R Phase Line One Broken

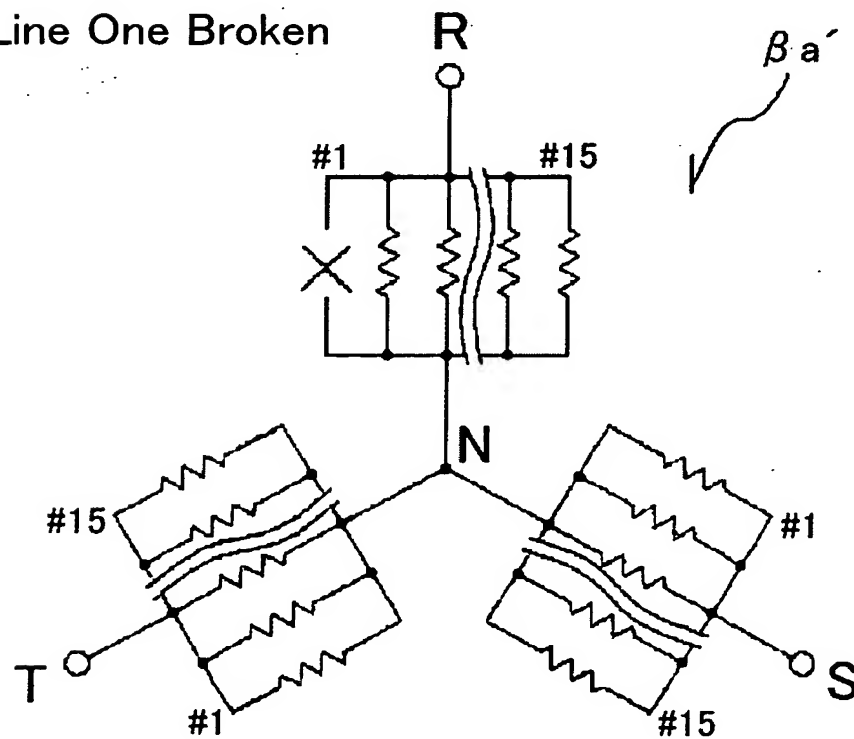


FIG. 24

Potential Rise by Short Circuit



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